**EE 480 Assignment 2: Multi-Cycle Gr8BOnd**

Implementor’s Notes

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**Abstract**

The project was a multi-cycle implementation of the instruction set design Gr8BOnd.

1. **General Approach**

The assignment was to design a Verilog multi-cycle implementation of the Gr8BOnd instruction set but not yet implement posit arithmetic. These instructions were treated as their equivalent integer operations. We also implemented an ALU to handle all arithmetic, logic, and integer-posit instructions.

1. **Issues**

Labels did not work as intended while testing our processor using assembly language. In place of labels, we used the actual addresses for branches and jumps.